

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1778149
<b>Application Number:</b>	10780140
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	9678
<b>Title of Invention:</b>	Method for optimization of logic circuits for routability
<b>First Named Inventor/Applicant Name:</b>	William E. Dougherty
<b>Customer Number:</b>	46069
<b>Filer:</b>	Frank Chau/Frank V. DeRosa
<b>Filer Authorized By:</b>	Frank Chau
<b>Attorney Docket Number:</b>	YOR920030437US1 (8728-653
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	8728-653-Amendment.pdf	496870	no	7

**Warnings:**

Information:		
<table border="1"><tr><td>Total Files Size (in bytes):</td><td>496870</td></tr></table>	Total Files Size (in bytes):	496870
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